

WEST Search History

DATE: Monday, October 11, 2010

Hide? Set Name Query
Hit Count

Interference

DB=PGPB,USPT,UPAD; PLUR=YES; OP=ADJ

| | | | |
|--------------------------|-----|---|-------|
| <input type="checkbox"/> | L20 | L18 and (virtual blocks).clm. | 0 |
| <input type="checkbox"/> | L19 | L18 and (virtual block).clm. | 0 |
| <input type="checkbox"/> | L18 | L17 and L15 | 4 |
| <input type="checkbox"/> | L17 | L14 and (graphical user interface).clm. | 86 |
| <input type="checkbox"/> | L16 | L14 and (user module).clm. | 14 |
| <input type="checkbox"/> | L15 | L14 and (user module).clm. | 14 |
| <input type="checkbox"/> | L14 | (microcontroller).clm. | 12680 |

Prior Art

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

| | | | |
|--------------------------|-----|-----------|---|
| <input type="checkbox"/> | L13 | L9 and L3 | 6 |
|--------------------------|-----|-----------|---|

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

| | | | |
|--------------------------|-----|----|-----|
| <input type="checkbox"/> | L12 | L6 | 20 |
| <input type="checkbox"/> | L11 | L5 | 524 |
| <input type="checkbox"/> | L10 | L9 | 0 |

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

| | | | |
|--------------------------|----|--|--------|
| <input type="checkbox"/> | L9 | L8 and programmable | 1042 |
| <input type="checkbox"/> | L8 | L7 and assembly | 1427 |
| <input type="checkbox"/> | L7 | L6 and counter | 3219 |
| <input type="checkbox"/> | L6 | L5 and (analog-to-digital or ADC) | 5861 |
| <input type="checkbox"/> | L5 | L4 and timer | 25697 |
| <input type="checkbox"/> | L4 | micro?controller or microcontroller | 150414 |
| <input type="checkbox"/> | L3 | (717/106 717/107 717/108 717/109 717/110 717/111 717/112 717/113).ccls. | 3556 |

| | | | |
|--------------------------|----|------------|---|
| <input type="checkbox"/> | L2 | 09/998,848 | 9 |
| <input type="checkbox"/> | L1 | 09/998848 | 9 |

END OF SEARCH HISTORY